

# Abstracts

## A K-Band Monolithic CPW Oscillator Co-Integrated with a Buffer Amplifier

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*K. Maruhashi, M. Madihian, L. Desclos, K. Onda and M. Kuzuhara. "A K-Band Monolithic CPW Oscillator Co-Integrated with a Buffer Amplifier." 1995 MTT-S International Microwave Symposium Digest 95.3 (1995 Vol. III [MWSYM]): 1321-1324.*

This paper describes the design approach, fabrication process, and performance of a monolithic CPW oscillator incorporating a buffer amplifier on the same chip, for K-band wireless network applications. At 21GHz, the output power of the oscillator recorded 17dBm with an overall de-*RF* efficiency of 22%. By changing the length of a source feedback line, the oscillation frequency was varied up to 26GHz. For all cases, the output power remained higher than 16dBm.

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